

MILITARY SPECIFICATION

SEMICONDUCTOR DEVICE, TRANSISTOR, NPN, SILICON, POWER
TYPES 2N6674, 2N6675, 2N6689, AND 2N6690
JAN, JANTX, AND JANTXV

This amendment forms a part of Military Specification MIL-S-19500/537(USAF), dated 22 October 1980 and is approved for use by the Department of the Air Force and is available for use by all Departments and Agencies of the Department of Defense.

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TABLE I, subgroup 2, Collector to emitter cutoff current, details column: Delete "Bias conditions D", and substitute "Bias conditions A".

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- * TABLE I, subgroup 5, Safe operating area (clamped switching), method column: Add "3053".
- * TABLE I, subgroup 5, Safe operating area (clamped switching), details column, delete and substitute:

" $T_A = 25^\circ\text{C}$; $V_{CC} = 15\text{ V dc}$ (see figure 6); Load Condition B; $R_{BB1} = 5\Omega$; $R_{BB2} = 1.5\Omega$; $V_{BB2} = 5\text{ V dc}$; $L = 50\ \mu\text{H}$; R of inductor = $.05\Omega$; and $R_{LOAD} = R$ of inductor."

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- * TABLE II, subgroup 5, thermal resistance, conditions column: Delete "4.5.3" and substitute "4.5.2".

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- * TABLE II, Subgroup 7, Safe operating area, (clamped switching) (destructive), conditions column, delete and substitute:

" $T_A = 25^\circ\text{C}$, $V_{CC} = 135\text{ V dc}$ (see figure 6); Load condition B; $R_{BB1} = 5\Omega$; $R_{BB2} = 1.5\Omega$; $V_{BB2} = 5\text{ V dc}$; duty cycle $\leq 5\%$; $L = 50\ \mu\text{H}$; R of inductor = $.05\Omega$; and $R_{LOAD} = 13.5\Omega$."

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- * FIGURE 7: Delete in its entirety.

NOTE: The margin of this amendment is marked with asterisks to indicate where changes (additions, modifications, corrections, deletions) from the previous amendment were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous amendment.

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