

The documentation and process conversion measures necessary to comply with this amendment shall be completed by 5 September, 2001.

INCH-POUND

MIL-PRF-19500/684  
AMENDMENT 1  
7 June 2001

## PERFORMANCE SPECIFICATION

SEMICONDUCTOR DEVICE, TRANSISTOR, FIELD EFFECT, SILICON, N-CHANNEL,  
RADIATION HARDENED (TOTAL DOSE AND SINGLE EVENT EFFECTS)  
TYPES 2N7472U2, 2N7473U2 AND 2N7474U2  
JANTXVR AND JANSR

This amendment forms a part of MIL-PRF-19500/684, dated 6 October 2000, and is approved for use by all Departments and Agencies of the Department of Defense.

PAGE 1

1.2, delete "SMD-2" and substitute "U2".

PAGE 3

FIGURE 1, title, delete "SMD2" and substitute "U2".

PAGE 7

4.5.2.b, delete "13.33 A" and substitute "16.67 A".

4.5.2.d, delete "15 V" and substitute "12 V".

PAGE 8

TABLE I, subgroup 2, thermal impedance, min limits column, delete "0.37" and substitute "0.38".

PAGE 9

TABLE I, subgroup 3, static drain to source "ON"-state resistance, 2N7472U2, max limits column, delete "0.026" and substitute "0.028".

PAGE 10

TABLE I, subgroup 7, reverse recovery time, 2N7474U2, max limits column, delete "775" and substitute "560".

FIGURE 3, delete and substitute:

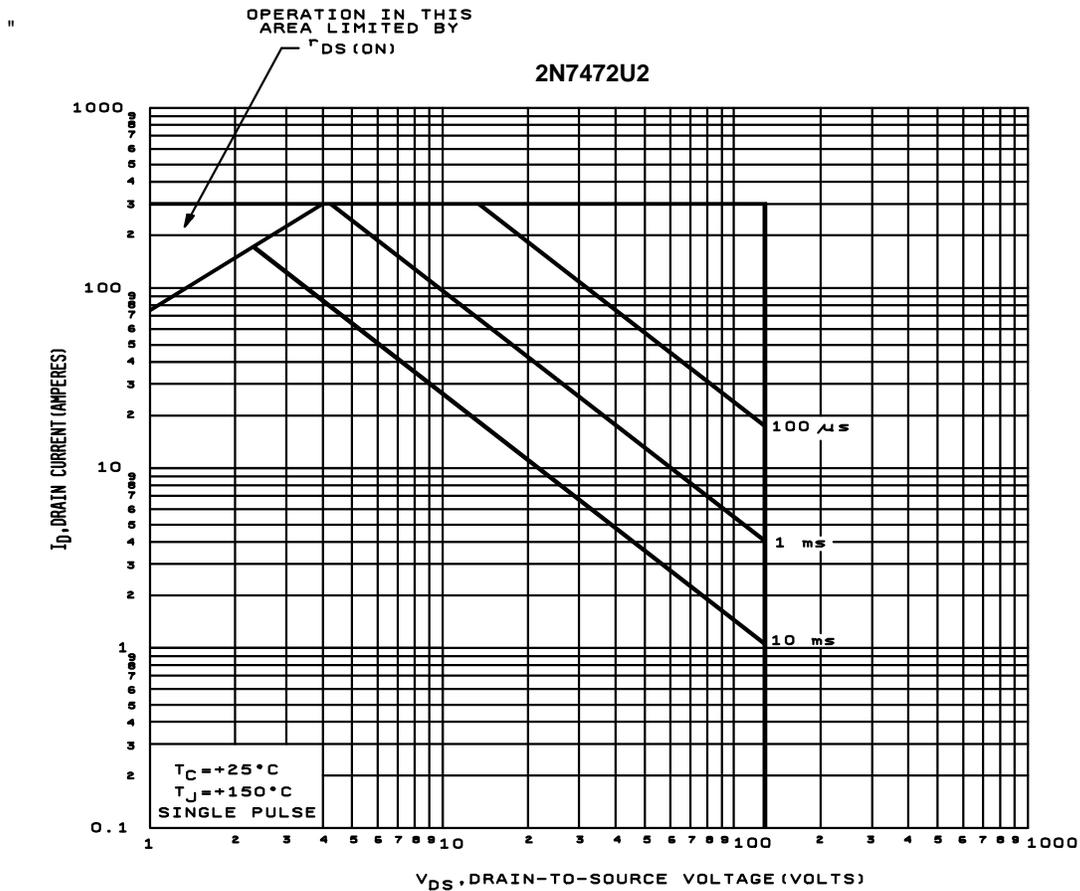


FIGURE 3. Safe operating area graph."

FIGURE 4, delete and substitute:

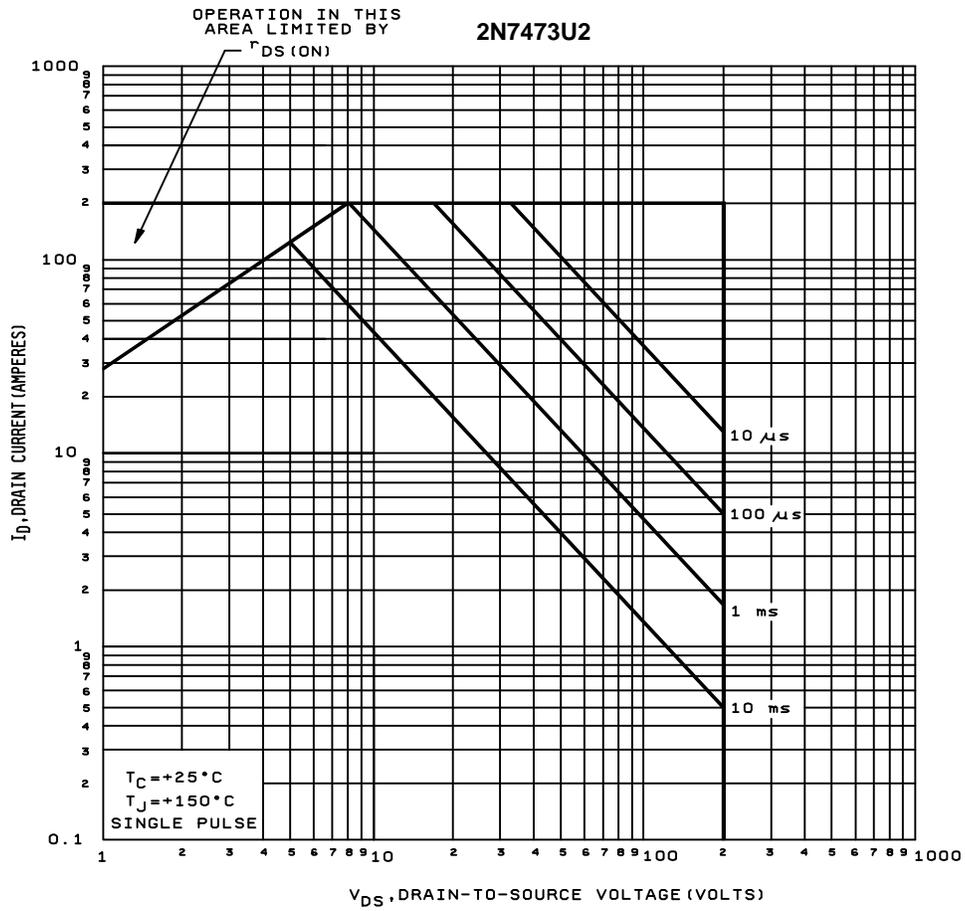


FIGURE 4. Safe operating area graph.

FIGURE 5, delete and substitute:

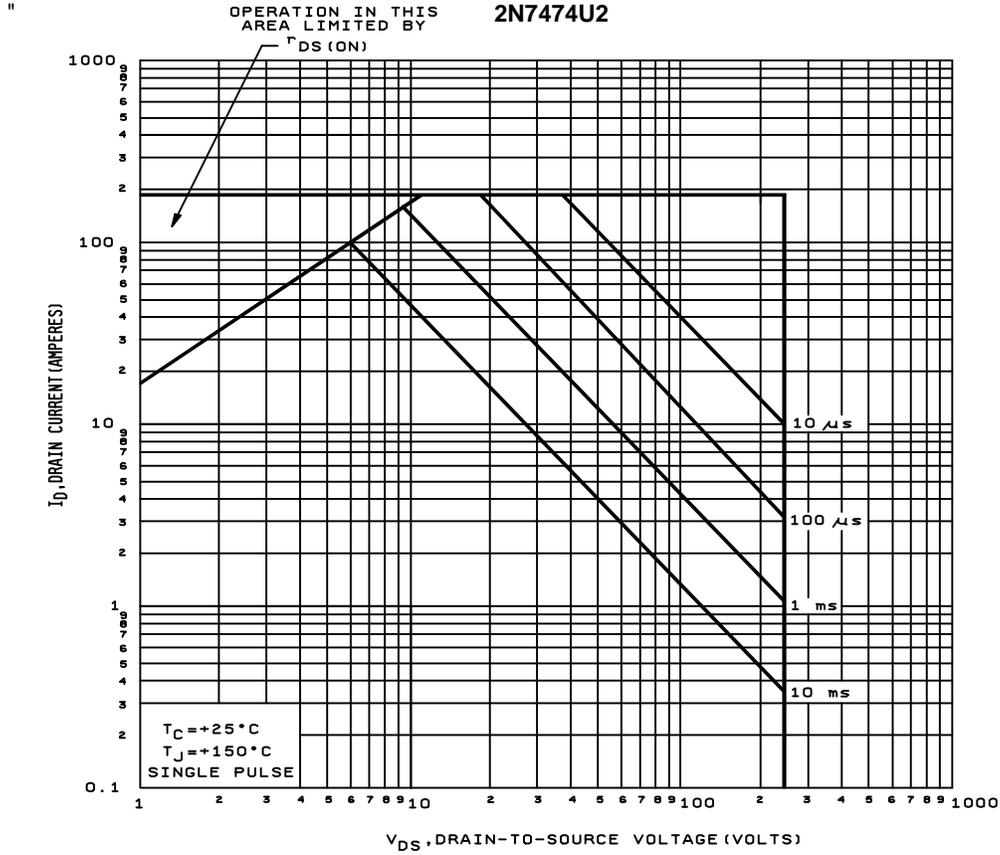


FIGURE 5. Safe operating area graph."

4.5.3.b, delete "13.33 A" and substitute "16.67 A".

4.5.3.d, delete "15 V" and substitute "12 V".

Custodians:  
 Army - CR  
 Navy - NW  
 Air Force - 11  
 NASA - NA  
 DLA - CC

Preparing activity:  
 DLA - CC  
 (Project 5961-2420)