

The documentation and process conversion measures necessary to comply with this amendment shall be completed by 6 November 2002.

INCH-POUND

MIL-PRF-19500/543F
AMENDMENT 1
6 August 2002

PERFORMANCE SPECIFICATION

SEMICONDUCTOR DEVICE, FIELD EFFECT TRANSISTORS, N-CHANNEL, SILICON
REPETITIVE AVALANCHE TYPES 2N6764, 2N6766, 2N6768, 2N6770,
JAN, JANTX, JANTXV, JANS, JANHC and JANKC

This amendment forms a part of MIL-PRF-19500/543F, dated 7 September 2001, and is approved for use by all Departments and Agencies of the Department of Defense.

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After 4.5.4, add:

"4.5.5 Gate stress test. Apply $V_{GS} = 30$ V minimum for $t = 250$ μ s minimum."

Custodians:
Army - CR
Navy - EC
Air Force - 11
DLA - CC

Preparing activity:
DLA - CC
(Project 5961-2655)

Review activities:
Army - MI
Navy - TD
Air Force - 19, 70, 99

AMSC N/A
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