

INCH-POUND

MIL-PRF-83532B
SUPPLEMENT 1
18 May 2001

PERFORMANCE SPECIFICATION

DELAY LINES, ACTIVE, GENERAL SPECIFICATION FOR

This supplement forms a part of MIL-PRF-83532B, dated 6 May 1999.

SPECIFICATION SHEETS

- MIL-PRF-83532/1 - Delay Lines, 14-Pin DIP Compatible, 5 Tap.
- MIL-PRF-83532/2 - Delay Lines, 14-Pin DIP Compatible, 5 Tap.
- MIL-PRF-83532/3 - Delay Lines, 14-Pin DIP Compatible, 10 Tap.
- MIL-PRF-83532/4 - Delay Lines, 14-Pin DIP Compatible, 3 Delay Circuits Per Package.
- MIL-PRF-83532/7 - Delay Lines, Active, 16-Pin DIP Compatible, Programmable 3-Bit, TTL Compatible, With Input Buffer.
- MIL-PRF-83532/8 - Delay Lines, Active, 14-Pin Surface Mount, 5 Tap.

Preparing activity:
DLA - CC

(Project 5999-0383)